

Docket No. 10001.000500
Response To Final Rejection
December 29, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Craig L. Stevens

Serial No. 09/888,017

Examiner: Karla A. Moore

Filing Date: June 21, 2001

Art Unit: 1763

Title: High Throughput Architecture For Semiconductor Processing

Honorable Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

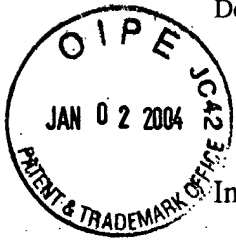
RESPONSE TO FINAL REJECTION

INTRODUCTORY COMMENTS

Sir:

This paper is responsive to the final rejection mailed on October 31, 2003
(hereinafter the "last office action").

Please amend the above-identified application as follows:



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